

REMARKS

This Preliminary Amendment cancels, without prejudice, claims 1-10 in the underlying PCT Application No. PCT/EP2004/053004 and adds new claims 11-20. The new claims, inter alia, conform the claims to United States Patent and Trademark Office rules and do not add any new matter to the application.

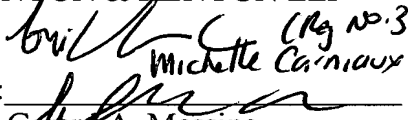
In accordance with 37 C.F.R. § 1.125(b), the Substitute Specification (including the Abstract) contains no new matter. The amendments reflected in the Substitute Specification (including Abstract) are to conform the Specification and Abstract to United States Patent and Trademark Office rules or to correct informalities. As required by 37 C.F.R. §§ 1.121(b)(3)(ii) and 1.125(c), a Marked-Up Version of the Substitute Specification comparing the Specification of record and the Substitute Specification also accompanies this Preliminary Amendment. Approval and entry of the Substitute Specification (including Abstract) are respectfully requested.

The underlying PCT Application No. PCT/EP2004/053004 includes an International Search Report, dated April 15, 2005, a copy of which is included. The Search Report includes a list of documents that were considered by the Examiner in the underlying PCT application.

It is respectfully submitted that the subject matter of the present application is new, non-obvious, and useful. Prompt consideration and allowance of the application are respectfully requested.

Respectfully Submitted,

KENYON & KENYON LLP

By:  (Reg. No. 36098)
Gerard A. Messina
Reg. No. 35,952

Dated: 13 July 2006

One Broadway
New York, NY 10004
(212) 425-7200
(212) 425-5288

CUSTOMER NO. 26646

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PIEZOELECTRIC ACTUATOR

~~Background Information~~ FIELD OF THE INVENTION

The present invention ~~is directed~~ relates to a piezoelectric actuator ~~according to the preamble of the main claim.~~ .

5 BACKGROUND INFORMATION

Piezoelectric actuators having piezoelectric ceramic layers stacked to form a multilayer stack are ~~known~~ described in, e.g., ~~from~~ German Patent Application No. DE 198 38 862 A1, the 1. The piezoelectric ceramic layers ~~having~~ have electrode layers, one
10 electrode layer being provided between each of the piezoelectric ceramic layers, an outer cover layer being provided on each end face of the piezoelectric actuator and the piezoelectric ceramic layers and the outer cover layers each having a predetermined dielectric constant. The outer cover layers are passive layers of
15 insulation and tolerance compensation and are made of unpolarized piezoceramic. The outer cover layers are machined to produce coplanar surfaces and have a greater layer thickness than the piezoelectric ceramic layers between the outer cover layers.

20 Piezoelectric actuators are advantageously triggered in cycles by a control unit at a high frequency. Due to the rapid switching operations, considerable electromagnetic interference is generated and must be reduced on the control unit or on the electric feeder lines to the piezoelectric actuators through complex
25 measures. For example, interference suppression is achieved by using interference suppression chokes and/or interference suppression capacitors on plug connections, snubber networks on switching elements and switching flank limiting measures or shielded and/or twisted electric feeder lines to the piezoelectric
30 actuators.

~~Advantages of the Invention~~

SUMMARY

5 ~~The~~A piezoelectric actuator according to an example embodiment of
the present invention ~~having the characterizing features of the~~
~~main claim has~~may have the advantage over the related art that
electromagnetic interference is easily reduced by having a lower
dielectric constant in the outer cover layers than in the
10 piezoelectric ceramic layers between the outer cover layers. This
makes it possible to reduce electromagnetic interference much more
effectively and less expensively than in the related art.

~~Advantageous improvements on and refinements of the piezoelectric~~
15 ~~actuator characterized in the main claim are possible through the~~
~~measures characterized in the dependent claims.~~

According to an advantageous example embodiment, the outer cover
layers are joined either to the actuator or to a cover of a cylinder
20 surrounding the actuator.

In the exemplary embodiments, the outer cover layers are
advantageously situated so that each has an electrode layer near
or adjacent to a ceramic layer. In a first advantageous embodiment,
25 the outer cover layers ~~according to the present invention~~ are each
provided on an electrode layer. The outer ceramic layers of a
piezoelectric actuator according to the related art are replaced
here by outer cover layers according to the present invention. No
additional process is necessary ~~here~~ in manufacturing the actuator.
30 In the case of an actuator according to the related art, the
electromagnetic interference may be reduced by additionally
arranging outer cover layers according to the present invention
on the outer ceramic layers of the actuator. This ~~requires~~may
require an additional procedure in the manufacture of the actuator
35 or before assembly of the actuator in its housing.

It ~~is~~may also be advantageous if the outer cover layers are
manufactured from a piezoelectric ceramic because in this way no

additional process or additional components are necessary in the manufacture of the piezoelectric actuator.

In an advantageous exemplary embodiment, relative dielectric
5 constant ϵ_r of the piezoelectric ceramic of the outer cover layers is decreased by admixture of suitable additives or by another change in the composition of the material.

It is also advantageous if the outer cover layers are each joined
10 to a ceramic layer by coating, gluing or soldering because these are particularly simple embodiments.

It ~~is very~~may be advantageous if the actuator is prestressed under pressure on the outer cover layers between two covers, where the
15 covers each have another insulating layer facing the outer cover layer of the actuator, this additional insulating layer having a lower relative dielectric constant ϵ_r than the outer cover layers of the actuator. The electromagnetic interference may be reduced in this way without having to alter the piezoelectric actuator
20 according to the related art.

Drawing

BRIEF DESCRIPTION OF THE DRAWINGS

25 Exemplary embodiments of the present invention are illustrated in simplified form in the ~~drawing~~figures and explained in greater detail ~~in the following description below.~~

Figure 1 shows a piezoelectric actuator according to a first
30 exemplary embodiment, ~~of the present invention.~~

Figure 2 shows a piezoelectric actuator according to a second exemplary embodiment, ~~and~~ of the present invention.

35 Figure 3 shows a piezoelectric actuator according to a third exemplary embodiment, of the present invention.

~~Detailed Description of the Exemplary Embodiments~~

DETAILED DESCRIPTION OF EXAMPLE EMBODIMENTS

Figure 1 shows schematically a piezoelectric actuator 1 composed of a plurality of stacked ceramic layers 2, forming a multilayer stack. The piezoelectric actuator extends in the direction of an actuator axis 6.

An electrode layer 3 provided between each of piezoelectric ceramic layers 2 is printed on ceramic layers 2 by a screen printing technique, for example. Ceramic layers 2 are polarizable and electrically non-conducting.

According to the first and second exemplary embodiments, actuator 1 has an outer cover layer 11 on each of its end faces. Outer cover layers 11 are connected to actuator 1 by an integrally bonded and/or frictionally locked method, for example. According to the first exemplary embodiment, outer cover layers 11 are designed as outer ceramic layers 16. Outer ceramic layers 16 are also referred to as passive layers because they are adjacent to an electrode layer 3 on only one side and have a non-polarized ceramic, for example. Ceramic layers 2 between outer ceramic layers 16 however are polarized, are each adjacent to two electrode layers 3 and therefore referred to as active layers.

Piezoelectric actuator 1 is designed in a cuboid or cylindrical shape, for example, ceramic layers 2 being square or circular in shape, for example. Electrode layers 3 are manufactured from electrically conducting material, e.g., silver-palladium, silver, copper or gold and are also referred to as inner electrodes.

Electrode layers 3 are positioned in such a way that they run alternately either from a first terminal face 4 of actuator 1 or from a second terminal face 5 of actuator 1 facing first terminal face 4, directed in the direction of ceramic layers 2 and extending up to close to particular opposing terminal face 4, 5. Electrode layers 3 directed from first terminal face 4 are electrically connected to a first outer electrode 8 and electrode layers 3 directed from second terminal face 5 are electrically connected

to a second outer electrode 9, outer electrodes 8, 9 being provided, e.g., in the form of an electrically conducting layer on terminal faces 4, 5. An electric voltage may be applied between outer electrodes 8, 9, the voltage being activated and deactivated for example via an electronic control unit 10, e.g., in a cycle. When a voltage is applied between outer electrodes 8, 9, a lengthening of actuator 1 by one stroke is achieved in a known way.

Piezoelectric actuator 1 is ~~known to be~~ formed by a cylinder 12 which is connected to electric ground and forms an actuator housing. Cylinder 12 is designed as a ~~known~~ conventional tubular spring, for example. An electrically non-conducting casting compound may be provided between cylinder 12 and actuator 1 to improve the dissipation of heat. Cylinder 12 is sealed at each end with a cover 15, covers 15 being joined to cylinder 12 in an integrally bonded and/or frictionally locked manner. Cylinder 12 and covers 15 are manufactured from metal, for example.

Actuator 1 is prestressed under pressure between covers 15, for example, i.e., cylinder 12 exerts a compressive force on actuator 1 via covers 15, so each end of actuator 1 is in contact with one of covers 15.

Actuator 1 is turned on and off in a high-frequency cycle by control unit 10 as a function of operating parameters, interference currents and interference voltages being generated unintentionally by interfering capacitances to ground. The electromagnetic interference induced by the interfering capacitances results in interference in electric equipment, e.g., radio reception, and ~~must~~ should therefore be suppressed.

For example, a first interfering capacitance C_1 is formed between cylinder 12 and actuator 1, whereby the side faces of electrode layers 3 facing cylinder 12 form a first capacitor plate and cylinder 12 forms a second capacitor plate. The space between cylinder 12 and actuator 1 forms a dielectric.

However, first interfering capacitance C_1 is small and generates only low electromagnetic interference because the side faces of electrode layers 3 facing cylinder 12 are small because of the small thickness of electrode layers 3 and the distance between cylinder 12 and actuator 1 is great. In addition, relative dielectric constant ϵ_r of the dielectric between cylinder 12 and actuator 1 is low. First interfering capacitance C_1 is not influenced by the present invention.

A second interfering capacitance C_2 prevails between covers 15 and electrode layer 3 nearest to particular cover 15, cover 15 constituting a first capacitor plate and electrode layer 3 nearest to cover 15 constituting a second capacitor plate. The dielectric of second interfering capacitance C_2 is formed by one of outer ceramic layers 16, situated between cover 15 and electrode layer 3 nearest to particular cover 15.

In contrast with first interfering capacitance C_1 , second interfering capacitance C_2 has a very high interference effect and is calculated from the equation

$$C_2 = \frac{\epsilon_0 \cdot \epsilon_{r16} \cdot A_{16}}{d_{16}},$$

where ϵ_0 is the dielectric constant *in vacuo*, ϵ_{r16} is the relative dielectric constant of outer ceramic layer 16, A_{16} is the end face of outer ceramic layer 16 and d_{16} is the thickness of outer ceramic layer 16. The thickness of outer ceramic layer 16 corresponds to the distance between cover 15 and electrode layer 3 nearest to particular cover 15.

To suppress interfering capacitance C_2 , area A_{16} and/or relative dielectric constant ϵ_{r16} of the dielectric may be reduced and/or thickness d_{16} of outer ceramic layer 16 may be increased.

An increase in thickness d_{16} of outer ceramic layers 16 is possible only within narrow limits because of the small construction height available and is thus possible only with a very minor effect.

- 5 An appreciable decrease in area A_{16} is impossible for reasons of mechanical strength and the power of the actuator.

According to example embodiments of the present invention, to reduce interfering capacitance C_2 , relative dielectric constant
10 ϵ_{r16} of outer cover layers 11 is decreased so that outer cover layers 11 have a lower relative dielectric constant ϵ_{r16} than piezoelectric ceramic layers 2 between outer cover layers 11.

Outer cover layers 11 are made of a material having a lower relative
15 dielectric constant ϵ_r than ceramic layers 2 between outer cover layers 11.

In this first exemplary embodiment, outer cover layers 11 are formed by outer ceramic layers 16 which are manufactured from a
20 piezoelectric ceramic whose relative dielectric constant ϵ_r is decreased, for example, by admixture of suitable additives or by another suitable change in the composition of the material. The composition of the ceramic of outer ceramic layers 16 varies in comparison with other ceramic layers 2 but the design is similar.

25 Relative dielectric constant ϵ_r of the outer ceramic layers of a piezoelectric actuator according to the related art is of the same size as relative dielectric constant ϵ_r of the ceramic layers between the outer ceramic layers.

30 According to example embodiments of the present invention, the outer ceramic layers of a piezoelectric actuator according to the related art are replaced by outer ceramic layers 16 having a lower relative dielectric constant ϵ_{r16} than piezoelectric ceramic layers

2 between outer ceramic layers 16. No additional process is necessary here in manufacturing the actuator.

Relative dielectric constant ϵ_{r16} of outer cover layers 11 is smaller than relative dielectric constant ϵ_{r16} of ceramic layers 2 by a factor of 10 to 100, for example.

Complex interference suppression measures in control unit 10 (e.g., shielding of the electric feeder lines to actuator 1 or electronic control unit 10) may be omitted thanks to the embodiment of the piezoelectric actuator according to an example embodiment of the present invention or such measures may be supplemented very effectively so that the same interference suppression effect is achieved more easily and less expensively than in the related art or a greater interference suppression effect is achieved with the same cost expenditure as in the related art.

Figure 2 shows a piezoelectric actuator according to a second exemplary embodiment. With the piezoelectric actuator according to Figure 2, parts that are the same as or have the same effect as the piezoelectric actuator according to Figure 1 are indicated with the same reference numerals. The piezoelectric actuator according to Figure 2 differs from the piezoelectric actuator according to Figure 1 in that an additional layer is applied to outer ceramic layers 16 of actuator 1, its dielectric constant ϵ_r being lower than dielectric constant ϵ_r of ceramic layers 2 between the additional layers.

In the second exemplary embodiment, piezoelectric actuator 1 has outer ceramic layers 16 which have the same composition as ceramic layers 2 between outer ceramic layers 16, as in the related art, and therefore have a relative dielectric constant ϵ_r at least approximately the same as that of ceramic layers 2 between outer ceramic layers 16. Outer ceramic layers 16 according to the second

exemplary embodiment have a greater thickness than ceramic layers 2 between outer ceramic layers 16, for example.

According to the present invention, an additional insulating layer 17 is provided on the ends on outer ceramic layers 16 of actuator 1, relative dielectric constant ϵ_{r17} of this additional insulating layer being smaller than relative dielectric constant ϵ_{r2} of ceramic layers 2. Additional insulating layers 17 are applied, e.g., by coating, gluing, soldering or the like to outer ceramic layers 16 of actuator 1 and are manufactured of glass, quartz, adhesive, lacquer, solder, silicon dioxide ceramic or equally suitable materials, for example. Additional insulating layer 17 is made of a rigid inelastic material.

Outer cover layer 11 in the second exemplary embodiment is formed by additional insulating layer 17.

The dielectric between one of two covers 17 and particular cover 15 of next electrode layer 3 in this second exemplary embodiment has two layers, outer ceramic layer 16 and additional insulating layer 17. This corresponds to a series circuit of two capacitances, so that interfering capacitance C_2 for the second exemplary embodiment is calculated as follows

$$\frac{1}{C_2} = \frac{1}{\frac{\epsilon_0 \cdot \epsilon_{r16} \cdot A_{16}}{d_{16}}} + \frac{1}{\frac{\epsilon_0 \cdot \epsilon_{r17} \cdot A_{17}}{d_{17}}}$$

where ϵ_0 is the dielectric constant in vacuo, ϵ_{r16} is the relative dielectric constant of outer ceramic layers 16, A_{16} is the end face of outer ceramic layers 16 and d_{16} is the thickness of outer ceramic layers 16, ϵ_{r17} is the relative dielectric constant of additional insulating layer 17, A_{17} is the end face of additional insulating layer 17 and d_{17} is the thickness of additional insulating layer 17.

Figure 3 shows a piezoelectric actuator according to a third exemplary embodiment. In the piezoelectric actuator according to Figure 3, the parts that are the same as those with respect to the piezoelectric actuator according to Figure 1 and Figure 2 or parts having a similar effect are labeled with the same reference numerals. The piezoelectric actuator according to Figure 3 differs from the piezoelectric actuator according to Figure 1 and Figure 2 in that additional insulating layer 17 is not attached to actuator 1 but instead is attached to one of covers 15 in an integrally bonded and/or frictionally locked manner.

According to an example embodiment of the present invention, covers 5 each have additional insulating layer 17. Additional insulating layer 17 is provided as outer cover layer 11 on an end face of cover 15 facing outer ceramic layer 16 of actuator 1. Additional insulating layer 17 of cover 15 according to this embodiment has for example an end face of the same size as ceramic layers 2 of actuator 1 or a larger end face than ceramic layers 2 of actuator 1.

Additional insulating layers 17 are attached to covers 15 of cylinder 12 by coating, gluing, soldering or the like and are made of glass, quartz, adhesive, lacquer, solder, silicon dioxide ceramics or equally suitable materials, for example.

According to the third exemplary embodiment, as in the second exemplary embodiment, actuator 1 has outer ceramic layers 16 which have the same composition as ceramic layers 2 between outer ceramic layers 16, for example, and therefore have at least approximately the same relative dielectric constant ϵ_r as ceramic layers 2 between outer ceramic layers 16.

The embodiment according to the third exemplary embodiment has the advantage that the design of actuator 1 remains unchanged and there is an improvement according to the present invention only at covers 15 of cylinder 12.

~~Abstract~~ ABSTRACT

Piezoelectric actuators having ceramic layers and electrode layers
are ~~known~~, and being provided with an outer cover layer ~~being~~
5 ~~provided~~ on each end face of the piezoelectric actuator, ~~and the~~.
The ceramic layers and the outer cover layers each ~~having~~have a
predetermined dielectric constant. Rapid switching of the actuator
results in considerable electromagnetic interference, which must
be reduced through complex measures involving the control unit or
10 electric lines. ~~This~~ The device reduces electromagnetic
interference by decreasing the interfering capacitance between the
actuator and the actuator housing connected to ground electrically.
The outer cover layers ~~(16, 17)~~ have a lower relative dielectric
constant than the ceramic layers ~~(2)~~ between the outer cover layers
15 ~~(16, 17)~~.
~~(Figure 1)~~